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CONFIRMATION NO. 5137

SERIAL NUMBER 10/752,504	FILING OR 371(c) DATE 01/08/2004 RULE	CLASS 714	GROUP ART UNIT 2138	ATTORNEY DOCKET NO. 247421US2	
APPLICANTS Yoshiyuki Tanaka, Kanagawa, JAPAN; Yoshinari Ojima, Kanagawa, JAPAN; ** CONTINUING DATA ***** <i>none pmc</i> ** FOREIGN APPLICATIONS ***** <i>yes pmc</i> JAPAN 2003-001741 01/08/2003 IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 04/09/2004					
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged <i>pmc</i> Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 6
ADDRESS 22850					
TITLE Semiconductor integrated circuit including operation test circuit and operation test method thereof					
FILING FEE RECEIVED 1158	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		